



MBS-GPS-OEMV1-104

The MBS-OEMV1-104 PC/104 GPS Module is a FPGA-based high speed 16C850 UART PC/104 Bus Compliant 8 BIT interface based upon the Exar XR16C2850 Dual UART with user selectable 128 Byte deep FIFO buffers and integrated with the OEMV-1 GPS. This interface provides two independent user inversion selectable TTL and RS232 level 1PPS outputs. Serial Port 1 is used as the Primary Data/Command Port and Port 2 is the Differential Input when a RTCM compliant correction message is supplied. Port 3 is a standard high speed serial port available for communications up to 921K Baud.

IO Base address is intuitively configured with Hexadecimal Rotary Switches. The user can then dynamically map the UART's base address and interrupt assignments via a series of software register write operations. All configuration registers are read/write.

FOR PRICING AND MORE INFORMATION, PLEASE CONTACT US.

FEATURES

- 128 Byte Deep FIFOs reduce system overhead
- Full 16 Bit Bus Address Decoding
- User configurable UART Address ranges from 0x0000 to 0xFFFF
- Interrupt levels 3, 4, 5, 6, 7, 9, 10, 11, 12, 14 & 15 available
- Supports full monitoring and control of 1PPS signals by either jumper or software
- DCD, DSR and CTS of all UART's dedicated as User TTL Inputs
- Supports the Novatel OEMV-1
- Can accept a FreeWave Diagnostic serial cable for external operation from a laptop
- Third RS-232 port up to 921K Baud for communication to external devices
- FPGA function customization available



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